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To Examiner A. Li Group Art Unit: 2183
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From Jason H. Vick Phone no. 703-610-8652
Date April 21, 2005 Time
File no. Attny Docket No.: T2146-906088 Pages including cover
Re: Appln. No. 09/242,974

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Attorney Docket No. T2146-906088

**AMENDMENT UNDER
37 CFR 1.116 - EXPEDITED
PROCESSING REQUESTED
EXAMINING GROUP 2183**

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APR 21 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application for:

First Named Inventor: UGON, MICHEL

Art Unit: 2183

Appln. No.: 09/242,974

Examiner: A. LI

For: UNPREDICTABLE MICROPROCESSOR OR
MICROCOMPUTER

Confirmation No.: 1762

* * *

**SECOND REQUEST FOR
RECONSIDERATION AFTER FINAL REJECTION**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the outstanding Official Action mailed November 16, 2004, and further to an April 20 Personal Interview, Applicant respectfully requests reconsideration of the above-identified application.

Applicant would like to thank Examiner Li for the courtesies extended to Applicant's representative, Mr. Jason Vick, during the April 20 Interview. During the Interview Independent Claim 20 was discussed and the features therein contrasted with Okin. Specifically, it was pointed out that independent Claim 20 is directed toward an unpredictable microprocessor or microcomputer. The unpredictable microprocessor or microcomputer includes a main memory, a first RAM-type working memory, a second RAM-type working memory, a processor, a bus and a

switching means. The main memory includes an operating system, a main program, and a secondary program, wherein the secondary program is not related to the main program. The processor is adapted to execute instructions from one or more of said main memory, said first working memory and said second working memory. The bus connects the processor to the main memory, the first working memory and the second working memory. The switching means makes the processor unpredictable, the switching means unpredictably jumping, while the programs are running, from one of the two working memories to the other working memory while saving the contents of the two working memories.

The switching means includes access registers associated with each of the main memory, the first working memory and the second working memory, at least one first block of registers stores the operating context of the programs in the main memory, and a switching circuit enables one of the working memories and controls the access registers associated with each of the main memory, the first working memory and the second working memory.

As discussed during the Interview, independent Claim 20 includes the structural features of a main memory. . . a first RAM-type working memory, a second RAM-type working memory, a processor . . . and a bus connecting the processor to the main memory, the first working memory, and the second working memory. Additionally, access registers are associated with each of the main memory, the first working memory and the second working memory.

As agreed to by the Examiner, Okin does not disclose a comparable structure.

Accordingly, at least based on the above structural differences, Okin does not teach or suggest the configuration as claimed. The remaining cited references fail to overcome this deficiency.

Accordingly, the rejection of Claim 20 is untenable and should be withdrawn. The claims that depend therefrom are also allowable over the references of record for at least the above reasons as well as the additional feature(s) recited therein. A prompt Notice of Allowance is respectfully requested.

The Commissioner is hereby authorized to charge to deposit account number 50-1165 (T2146-906088) any fees under 37 CFR § 1.16 and 1.17 that may be required by this paper and to credit any overpayment to that Account. If any extension of time is required in connection with the filing of this paper and has not been separately requested, such extension is hereby requested.

JHV:jab

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April 21, 2005

Respectfully submitted,

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CERTIFICATE OF FACSIMILE TRANSMISSION

I hereby certify that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office on April 21, 2005.

Jason H. Vick, Reg. No. 45,285